The most powerful and flexible JPEG 2000 solution available today

intoPIX EMMY® Award-winning JPEG 2000 Encoder and Decoder IP-Cores respect and protect high value images. Handling simultaneously deep color, low and high data rates and extensive encoding know-how, intoPIX IP-Cores enable best-in-class picture quality.

The IP-Cores are available for the most recent FPGA platforms and ensure lower consumption, lower temperature dissipation and lower bill of material with unprecedented performances.

Fully benefiting from a modular architecture (image formats, frame rates and resolutions) and completed with a wide range of companion IP-Cores (video transport, security, memory sharing, ...), the intoPIX solutions provide an easy, timely and cost-effective way to implement JPEG 2000 technology.

High Quality Picture
> From Lossy to Lossless compression

Customizable Design
> Single chip FPGA
> Artix-7
> Kintex-7
> Zynq-7
> Virtex-7

Cost Effective
> Reduced BOM
> Ultra dense
> Optimized per application

Future-Proof
> HD & ULTRA-HD 4K & 8K
> High Frames Rates for cinema and slow-motion
> Zero-Latency mode (<5ms) for video over IP
> J2K-Raw (Bayer) for cameras

Companion IP-Cores
> DDR2/DDR3 Memory Controller
> MPEG2-TS Layer

Security IP-Cores
> AES Security IP-Cores
> RSA Security IP-Cores
> Hash Function

Easy integration
> J2K HDK on Xilinx reference boards
> Team training
> Application reference designs

www.intopix.com

Take IMAGING to the NEXT LEVEL
The HD Family
JPEG 2000 for Content Production

The JPEG 2000 HD cores allows you to preserve image quality throughout your complete production process.

These IP-Cores have a decoded bitrate range from 250 Mbps up to 1 Gbps, and can process up to 120 progressive frames per second in HD resolution (1920 x 1080).

Moreover, the IP-Cores flexibility enables to address all known broadcast standards in terms of resolution and frame rates.

- Single Chip
- Broadcast and IMF profile compliant (JPEG 2000 Part 1 Amd3 and AmdB)
- Any resolution up to 1080p-120 fps
- Up to 1Gbps Compressed Bitrates
- Progressive/interlaced
- 4:2:2/4:4:4
- Easy 3D stereoscopy
- Multi streams support
- CBR/VBR

APPLICATIONS
Camera embedded encoder
Field recorder
Production video server
Fill and Key playout server
High Speed server for Slow-Motion
VSF TR01 - Transport of J2K in MPEG-2 TS over IP

Ultra Low Latency Option
JPEG 2000 for Live streams

The HD family also offers an Ultra Low Latency option that enables you to carry HD streams within the network with a point-to-point latency below 5 milliseconds while guaranteeing high image quality.

- Sub intra-frame latency
- Visually lossless quality

APPLICATIONS
SMPT2022 / AVB
Video over IP and Contribution
Wireless Transmission
Remote Control & Monitoring
IP-based production

The Lossless Family
JPEG 2000 for pristine content

The intoPIX Lossless encoders and decoders preserve the original image information. They support any image format up to 4K+ (4096x3112) with color depth up to 12 bit per component and JPEG 2000 Lossless compression.

- Single Chip
- Fully flexible image format up to 4K single tile
- Math. Lossless Encoding

APPLICATIONS
Archiving
Store and Forward
Medical
Aerospace
Geospace

The Digital Cinema Family
DCI compliant IP-Cores & HFR

The intoPIX Digital Cinema IP-Cores are optimized to meet the highest requirements of Digital Cinema including 2K-120 fps and 4K-3D formats.

The Digital Cinema solution enables the integration within a single chip of a complete image processing chain, i.e. decryption-decoding-watermarking and encryption, together with video I/O interfaces, system control and system interfaces.

- Single Chip
- DCI compliant (JPEG 2000 Part 1 Amd 1)
- 2K resolution at up to 120 fps
- 4K resolution at up to 30 fps
- Bitrates up to 1Gbps

APPLICATIONS
Cinema Mediablock (DCI & HFR)
Cinema Post-production
Faster than real-time encoder
The J2K-RAW Family
Best bayer pattern images compression

intoPIX J2K-RAW compression IP-cores preserve perfectly the RAW output (Bayer pattern images) of Bayer-filter cameras with best-in-class JPEG 2000 compression. Offering a visually lossless quality, J2K-RAW allows also an automatic access to low-resolution preview thanks to the JPEG 2000 scalability.

- Compact J2K-RAW IP-cores
- Wavelet-based standardized compression
- Visually Lossless and VBR support
- HD, 4K, 8K bayer pattern images at multiple frame rates
- Higher image quality possibilities within a reasonable storage and bandwidth
- Fast low-resolution preview access thanks to the JPEG 2000 scalability.

Applications
Cameras (HD, 4K, 8K,...)
High-Speed Cameras
Recorders
Production and Post-production Servers

Companion IP-Cores
intoPIX proposes companion IP-Cores specially designed to ease the integration of JPEG 2000 and minimize your time-to-market.

DRAM Memory controller IP-Cores
DDR3 and DDR2 cores
The IPX-DDR2 and IPX-DDR3 IP-cores match various operating frequencies and physical bus size (8, 16, 32 or 64 bit), allowing you for instance to respectively reach peak transfer rate of 34Gbit/s and 68 Gbit/s on 64 bit wide interface. These IP-cores are fully-optimized to provide a powerful interface towards the JPEG 2000 IP-cores.

Multi-core memory bridge core
The IPX-MLB is an efficient IP-core enabling multiple JPEG 2000 IP-cores to share a memory access with a single controller. Thanks to the IPX-MLB, it is easy to boost a design that needs to run multiple JPEG 2000 cores in parallel. The IPX-MLB also enables to share the memory access with other processes through an AXI4 interface.

Evaluation and Integration
intoPIX offers many different possibilities to assess the quality, capability, characteristics and to speed-up the integration of a fully functional JPEG 2000 core.

Hardware Development Kit (HDK)
intoPIX HDK enables faster and reliable integration. Using a flexible structure, it focuses on providing a seamless environment for the integration of any intoPIX JPEG 2000 IP-cores, using a FPGA reference board as starting point. It validates efficiently the integration with a smart step-by-step approach and accelerates the progress towards the final customer application even before the dedicated hardware is ready. Thanks to a layered structure, it significantly eases the porting from one hardware platform to the other, and facilitates switching between applications in the same hardware environment.

- Standalone implementation of the custom IP-core flavour on a FPGA reference board
- Fastest product time to market
- Increased integration friendliness and design re-usability

Security IP-Cores
AES Encryption-Decryption Cores
The IPX-AES Modules are encryptor-decryptor IP-Cores providing an efficient FPGA implementation of the Advanced Encryption Standard (AES).

Supporting a wide range of bitrates and combining several functions and operating modes, the IPX-AES cores can be customized for each specific application.
More on: www.intopix.com/security

Hash function Core
The IPX-HMAC-SHA-1 IP-Core is the hashing function required for content integrity check and content identification as specified in DCI documents. It enables computation of the keyed-hash message authentication code (HMAC) for audio and video assets.

RSA Public Key Cryptography Accelerator Core
The IPX-RSA modular exponentiation accelerator is an efficient and low footprint arithmetic coprocessor for the RSA public-key cryptosystem. It performs the modular exponentiation calculation and therefore offloads the most computer-intensive operation of RSA from the main processor. The RSA cryptosystem can be used for public key encryption, decryption and signature/authentication.

Watermarking Memory Bridge Core
The IPX-WB watermarking memory bridge offers the possibility to connect seamlessly the Civolution Nextguard DCI Video Watermarking IP-core with intoPIX JPEG 2000 IP-cores and to share the same external memory.

Application reference designs
Additionally to the HDK, intoPIX also accelerates customer product developments with video application reference designs. The reference designs aim to bring powerful proof of concepts using intoPIX high-performance IP-cores.

- SMPTE2022 JPEG 2000 video over IP
- UHD4K JPEG 2000 acceleration
- ...

For more information on the available possibilities, please contact sales@intopix.com
Image features
- Bit depth: 8, 10, 12, 14, 16
- Color Space: Any (RGB, YUV, XYZ, YCbCr,...)
- Interlaced field, progressive frame
- Monochrome, 3 and 4 components, Bayer pattern
- Any resolutions (SD, HD, 2K, 4K, 8K, ...)

JPEG 2000 (ISO 15444-1)
- Wavelet transforms: 5/3 and 9/7
- Reversible and irreversible color transforms
- Decomposition levels: up to 6 levels
- Quantization steps: programmable per level and per component
- Quality layer: 1 layer
- Digital Cinema (DCI) compliant - JPEG 2000 Part 1 Amd 1
- Broadcast profiles compliant - JPEG 2000 Part 1 Amd 3
- IMF profiles compliant - JPEG 2000 Part 1 Amd 8
- Tiling: Single tile
- Progression Order: CPRL
- Code Block Size: 32x32, 64x32, 64x64, 128x32
- Contrast sensitivity function

Quality and Bit Rate Control
- Maximum codestream bitrate: 250 Mbps, 500 Mbps, 1 Gbps, 2 Gbps and 8 Gbps for Lossy compression and unlimited for Lossless compression
- Variable bit rate (VBR): The overall bit rate is variable for a selectable constant quality
- Capped VBR: 9/7 wavelet filter - Constant quality but variable bit rate is capped at a given maximum limit
- Visually Lossless VBR: 9/7 wavelet filter - Constant Visual Quality - No visually noticeable artifact - 8:1 compression, used up to 20:1

Latency
- Low latency: configurable from 1 to 2 frames (fields) at encoding; from 0.5 to 1 frame(field) at decoding (ie. 1080p60 end-end is maximum 50ms)
- Ultra-Low latency (Sub-I-frame): down to 1/16th of a frame (field) at encoding and to 1/16th of a frame(field) at decoding with tile border protection (ie. down to 5 milliseconds with end-to-end compression in 1080p60)

Control
- Encoder:
  - Up to 64 preloaded configurations and frame per frame control
  - Configuration control: through control bus or through video interface
  - Real-time access to status registers for monitoring and debug
- Decoder:
  - Up to 16 preloaded channel configurations
  - Configuration control: through control bus or through Code stream control packets
  - Real-time access to status registers for monitoring and debug

Hardware requirement and delivery:
- External memory: support DDR3, DDR2, LPDDR2, DDR4 (coming)
- FPGA: support the latest Xilinx 7 series and previous 6 and 5 series.
- Fully customizable IP-core per application, delivered and silicon proven with intoPIX HDK for fast integration

Target Xilinx FPGA

<table>
<thead>
<tr>
<th>IP-cores EXAMPLES</th>
<th>Max resolution</th>
<th>Max frame rate fps</th>
<th>Max Bitrate</th>
<th>Virtex-7 VII/VIX</th>
<th>Kintex-7 K</th>
<th>Artix-7 A</th>
<th>Zynq Z</th>
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<tr>
<td>IPX-IP-1HD-Enc</td>
<td>1920x1080</td>
<td>30p (60)</td>
<td>250 Mbps</td>
<td>290T-1/330T-1</td>
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FPGA IP-Cores
- Fully customizable IP-core per application, delivered and silicon proven with intoPIX HDK for fast integration

Contact intoPIX for your own custom IP-core configuration

HEADQUARTERS: intoPIX SA
Rue Emile Francqui 9
B-1438 Mont-Saint-Guibert - Belgium
Tel: +32 10 23 84 70
Fax: +32 10 23 84 71
sales@intopix.com

USA: intoPIX America inc.
300 Arboretum Place
Suite 680
Richmond, VA 23236 - USA
sales@intopix.com

CHINA: sales.beijing@intopix.com
sales.shanghai@intopix.com

INDIA: sales.india@intopix.com

ISRAEL: sales.israel@intopix.com

JAPAN: sales.japan@intopix.com