### SUMMARY


This reference application is using standardized Altera IP-cores interfaces in order to enable a fast integration in your design. It is easy to port the full design, to customize it or to use only the IP-cores necessary for your application.

### BENEFITS

- **JPEG 2000 IP-cores for low cost FPGAs**
- Full HD 60fps • Visually Lossless compression • Low latency
- Standard Interfaces (Avalon® ST) to connect to VIP
- (Video & Image processing suite) for fast prototyping
- Reference application and IP-cores available from intoPIX
- High quality compression and transmission
- Compact codec – Low footprint implementation
JPG 2000 ENCODER
1920x1080 up to 60hz

HDMI Receiver Daughter Card

RJ45 out

Up to 1Gbps transmission

JPG 2000 DECODER

HDMI Transmitter Daughter Card

RJ45 in

APPLICATIONS

- KVM extenders, Remote display controllers, Video over IP
- Professional Wireless Transmission
- Built in camera compression
- Transfer of High Resolution images in Limited Bandwidth: use Standard connections/components with low complexity codec in small FPGA devices