FlinQ
Flawless Link Quality

Discover FlinQ for wireless displays

FlinQ™ technology is an innovative image processor invented by intoPIX that enables higher quality and a significant gain of efficiency for high performance and reliable visual communication. FlinQ™ technology delivers flawless video quality and display transmission over wireless links at the speed of light.

It opens up a realm of new capabilities to manage pixel perfect 4K and 8K display and video content, leveraging existing IT infrastructures and networks with very low power consumption, while safeguarding micro-second latency.

OPTIMIZED FOR ALL TYPE OF SCREEN-CONTENT

Excel tables, fine text, natural video, CGI, video games, ...

FlinQ

Technology benefits & Applications

- Flawless imaging profile on any content : patented display and video modes enable recovery of all the original information
- Pixel perfect quality : Natural video, CGI, and fine text without compression artefacts
- Extremely low power : Extremely low resource and memory usage
- No latency : 1/10th of a millisecond (imperceptible to human)
- High flexibility : Any resolution, color depth or frame rate - multi-video & display-format management

Optimal bandwidth for wireless displays

- **Down to 200 Mbps** (**pixel perfect** at 450Mbps/4K screen) and **scalable up to 8K wireless** (**pixel perfect** at 1 Gbps)
- Optimal for latency critical applications : virtual desktop, live (second) screen sharing, wireless TV, gaming,
- Optimal for wireless protocols such as WiGig, 5G, Wifi-6 ...

IP-core & SDK implementations specifications

VIDEO I/O	**Color format**	RGB, YCbCr (SDR & HDR support)
	Color subsampling	4:4:4, 4:2:2, 4:2:0
	Bit depth	8 / 10 / 12 bits per component
	Resolution	Any resolution up to 8K
	Frame rates	60fps / 120fps
FlinQ	**Quality**	Pixel Perfect Quality
	Rate control / Latency	CBR (constant bit rate) or VBR (variable bit rate) modes Less than 1 frame (only few lines) of latency
	Additional Feature	Embedded downscaler in decoder

IMPLEMENTATION	FPGA / ASIC IPs	CPU / GPU SDKs
	Encoder and decoder have the same complexity	Real-time operation guaranteed (no overflow or underflow)
	Customizable to your configurations	Powerful, real-time
	Various pixel per clock implementations	Common APIs

Intel FPGA : Cyclone V, Arria V, Stratix V, Cyclone 10, Arria 10, Stratix 10, Agilex

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